-Nombre de la empresa/organización: Synopsys Chile
-Breve descripción de la empresa/organización: At Synopsys, we’re at the heart of the innovations that change the way we work and play. Self-driving cars. Artificial Intelligence. The cloud. 5G. The Internet of Things. These breakthroughs are ushering in the Era of Smart Everything. And we’re powering it all with the world’s most advanced technologies for chip design and software security
-Nombre y datos de contacto: Diana Guerrero / diana.guerrero@synopsys.com
-Plazo de postulación: 60 días

27355BR: R&D Engineer Senior (Equipo PV) 1 vacante

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For international company, we are looking for a R&D Engineer position:

- The candidate will be responsible for developing, applying, and maintaining quality standards for company products. Develops and executes software test plans detecting the issues and their root causes. Analyzes and writes test standards and procedures. Maintains documentation of test results to assist in debugging and modification of software. Analyzes test results to ensure existing functionality and recommends corrective action.
- The candidate will be joining the PV (Product Validation) Chile Team. The team works on the validation of the Synopsys tools used for RTL Synthesis, Test, Physical Implementation (Place and Route), Formal Verification and Sign-off. We are responsible of the quality of the tools and we focus on the stability, performance, usability and QoR (Quality of Results). The team interacts daily with colleagues and internal/external customers in different parts of the world (mainly US and China).
- This position is specifically to work on Fusion Compiler tool, the Singular RTL-to-GDSII Digital Implementation Solution of Synopsys.

Minimum requirements

- BS or MS in Computer Science / Electronics or equivalent with 0 – 6 years of experience in software testing.
- Knowledge in scripting and programming languages.
- Knowledge in GNU/Linux environment.
- Knowledge in digital systems.
- Very good analysis and problem-solving skills.
- Excellent teamwork skills.
- Good communication skills in English.

Preferred requirements

- Knowledge in one scripting language (TCL, Python, Perl, etc.)
• Knowledge in one programming language (C, C++, C#, Java, etc.)
• Knowledge in one GNU/Linux shell (Bash, CSH, TCSH, KSH, etc.)
• Knowledge in one RTL language (Verilog, VHDL, etc.)
• Experience working with worldwide cross-functional teams.
• Knowledge of software development processes.

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Job Description and Requirements:

Responsible for designing, developing, troubleshooting, or debugging software programs. Develops software tools including operating systems, compilers, routers, networks, utilities, databases and internet-related tools, etc. Determines hardware compatibility and/or influences hardware design.

Experience in programming software for operating systems, utilizing machine assembly and/or job control languages, and some knowledge of software capabilities. Designs algorithms and data structures. Experience on development of complex software projects, familiarity with C/C++ coding, and a strong background in data structures and algorithms. Has strong desires to learn and explore new technologies and demonstrates good analysis and problem-solving skills. Prior knowledge and experience of CAD tool development are required.

Typically requires a minimum of 2 years of related experience. Usually developing professional expertise, and may apply company policies and procedures to resolve a variety of issues. At a minimum, has working knowledge of work area and general proficiency with tools, systems, and procedures required to accomplish the job. Exercises judgment to determine appropriate action. Implementations and solutions are reviewed for accuracy and overall adequacy. Builds productive internal/external working relationships. Contacts are primarily within business unit and occasional organizational and external customer contacts on routine matters.

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27679BR: Applications Engineer II (Equipo AE) 1 vacante

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Our Silicon Design & Verification business is all about building high-performance silicon chips—faster. We’re the world’s leading provider of solutions for designing and verifying advanced silicon chips. And we design the next-generation processes and models needed to manufacture those chips. We enable our customers to optimize chips for power, cost, and performance—eliminating months off their project schedules.

We’re looking for an Applications Engineer to join the team. Does this sound like a good role for you?

You will be a member of the Product Applications Engineering team for Synopsys synthesis products. Responsibilities include providing technical support to field specialists and support center.

**Major tasks involve:**
- Support of new and existing products within synthesis-related tools.
- Daily interaction with product development R&D, and field support.
- Works on key customer issues and may work across product teams with other products such as IC Compiler, Prime Time, Power Compiler, Test Solutions products etc.
- Writes and reviews design flow and methodology specifications, and requirement specifications.
- Develops test plans and test cases and executes on the test plans at the functional level.

**Key Qualifications**
- BS/MS Electrical Engineering, Computer Engineering.
- The ideal candidate understands the basic overall functional chip design process, is proficient with UNIX, RTL(Verilog/VHDL) and has a working knowledge of the ASIC design flow and/or CAD engineering combined with synthesis and design methods.
- Must have working knowledge of scripting languages, i.e. Python, Perl, Tcl, etc.
- Must have strong problem solving, debug and analysis skills.
- Strong communication skills required (written, oral and presentation).
- Some travel may apply.

**Preferred Experience**
- Requires a minimum of 2-4 years related experience.
- Knowledge of some EDA tool products and product knowledge in the area of synthesis, verification, scan and physical design is preferred.

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Job Description and Requirements:

Responsible for designing, developing, troubleshooting, or debugging software programs. Develops software tools including operating systems, compilers, routers, networks, utilities, databases and internet-related tools, etc. Determines hardware compatibility and/or influences hardware design.

Experience in programming software for operating systems, utilizing machine assembly and/or job control languages, and some knowledge of software capabilities. Designs algorithms and data structures. Experience on development of complex software projects, familiarity with C/C++ coding, and a strong background in data structures and algorithms. Has strong desires to learn and explore new technologies and demonstrates good analysis and problem-solving skills. Prior knowledge and experience of CAD tool development are required.

Typically requires a minimum of 5 years of related experience. Possesses a solid understanding of specialization area plus working knowledge of one other related area. Resolves issues in creative ways. Exercises independent judgment in selecting methods and techniques to obtain solutions. Executes projects from start to completion. Contributes to moderately complex aspects of a project. Determines and develops recommendations to solutions. Works on team-driven or task-oriented projects. May guide more junior peers with aspects of their job. Networks with senior internal and external personnel in own area of expertise.

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Have you ever dreamed of working with the top technology enterprises of the world, developing technology to enable the innovations of tomorrow? You can make it a reality working at Synopsys because we are looking for you to help us shape tomorrow's world. We are at the heart of the innovations that will change the way we work and play. Self-driving cars. Artificial Intelligence. The cloud. 5G. The Internet of Things. We are powering it all with the world’s most advanced technologies for chip design and software security. If you share our passion for innovation, we want you!

**Description**

You will be responsible for the design, implementation, and debugging of the pieces of software lying at the core of our world-leading microchip design tools. You will work with a strong background in C and C++, data structures, and algorithms. You will design regression tests to assure continuous levels of quality within a large community of developers. You will be working in a worldwide enterprise, meeting with talented people from around the world, partnering to solve complicated issues. You will report to a local manager, working alongside the US team. And the most important, you will never stop learning and surprise you!

**Qualifications**

BS in Computer Science / Electronics or equivalent  
Minimum 2 years of related experience  
Working knowledge of C/C++ and Linux  
Problem solving skills  
Good communication skills in English (spoken and written)

We are looking forward to hearing from you!

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27969BR / 27969BR: R&D Engineer Senior (Equipo CG) 2 vacantes

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Our Silicon Design & Verification business is all about building high-performance silicon chips—faster. We’re the world’s leading provider of solutions for designing and verifying advanced silicon chips. And we design the next-generation processes and models needed to manufacture those chips. We enable our customers to optimize chips for power, cost, and performance—eliminating months off their project schedules.

We're looking for a Senior R&D Engineer to join the team.

Does this sound like a good role for you?

Responsible for designing, developing, troubleshooting, or debugging software programs. Develops software tools including operating systems, compilers, routers, networks, utilities, databases and internet-related tools, etc. Determines hardware compatibility and/or influences hardware design.

Key Qualifications

- Designs algorithms and data structures. Has strong desires to learn and explore new technologies and demonstrates good analysis and problem-solving skills.
- Prior knowledge and experience of CAD tool development are required.
- Possesses a solid understanding of specialization area plus working knowledge of one other related area. Resolves issues in creative ways.
- Exercises independent judgment in selecting methods and techniques to obtain solutions.
- Executes projects from start to completion.
- Contributes to moderately complex aspects of a project.
- Determines and develops recommendations to solutions.
- Works on team-driven or task-oriented projects.
- May guide more junior peers with aspects of their job.
- Networks with senior internal and external personnel in own area of expertise.

Preferred Experience

- Experience in programming software for operating systems, utilizing machine assembly and/or job control languages, and some knowledge of software capabilities.
- Experience on development of complex software projects, familiarity with C/C++ coding, and a strong background in data structures and algorithms.
- Typically requires a minimum of 5 years of related experience.
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28202BR: R&D Engineer II (Equipo UPF) 1 vacante

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We’re looking for a R&D Engineer to join the team.
Does this sound like a good role for you?
Responsible for designing, developing, troubleshooting, or debugging software programs. Develops software tools including operating systems, compilers, routers, networks, utilities, databases and internet-related tools, etc. Determines hardware compatibility and/or influences hardware design.

Key Qualifications

- Designs algorithms and data structures.
- Has strong desires to learn and explore new technologies and demonstrates good analysis and problem-solving skills. Prior knowledge and experience of CAD tool development are required.
- Exercises judgment to determine appropriate action.
- Implementations and solutions are reviewed for accuracy and overall adequacy.
- Builds productive internal/external working relationships.
- Contacts are primarily within business unit and occasional organizational and external customer contacts on routine matters.

Preferred Experience

- Typically requires a minimum of 2 years of related experience. Usually developing professional expertise and may apply company policies and procedures to resolve a variety of issues.
- At a minimum, has working knowledge of work area and general proficiency with tools, systems, and procedures required to accomplish the job.
- Experience in programming software for operating systems, utilizing machine assembly and/or job control languages, and some knowledge of software capabilities.
- Experience on development of complex software projects, familiarity with C/C++ coding, and a strong background in data structures and algorithms.

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Job Description:
You will be an integral member of the product engineering team focused on shaping the direction and development of physical synthesis products at Synopsys.

Responsibilities:

As a Product Engineer (PE) you will collaborate with R&D, field sales/support teams and key customers on leading edge front-end physical synthesis products and chip design customer flows. You will evaluate and understand digital design implementation challenges and needs, and provide feedback to define and shape the product roadmap. You will be responsible for creating requirement specifications to drive product innovation and developing methodology/usage roll out and support collateral. As a product expert, you will also drive and provide proactive technical support, primarily to internal customers (field application engineering team) and occasionally directly to Synopsys customers, during product evaluation, deployment as well as post-sales activities.

You will work in close collaboration across cross functional and cross product global teams on a regular basis to ensure alignment and completeness of a full flow design methodology. Occasional traveling is required.

Basic Qualifications

- BS/MS Electrical Engineering, Computer Engineering
- Good scripting (Python/Perl/Tcl) skills and must be conversant with UNIX;
- Knowledge of C/C++ is a plus

Requirements:

- Good knowledge of digital electronics concepts and familiarity with ASIC design flow, and/or VLSI design methodologies is required
- Ability to work in a fast paced, technology-driven environment and strong motivation to continuously learn and apply advanced digital design concepts and best practices
• Strong analytical and problem-solving skills with ability to clearly narrow down and summarize findings
• Experience with HDL (Verilog/VHDL) and Electronic Design Automation products in the area of synthesis and physical implementation, especially with Synopsys, Cadence, or Mentor tools
• Excellent verbal and written communication skills including the ability to interface with customers and product team
• Ability to multi-task and work in close collaboration across cross functional and global team environments

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283248R: Product Engineer (Equipo PE) 1 vacante

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Job Description:
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Responsibilities:
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Basic Qualifications:
- BS/MS Electrical Engineering, Computer Engineering
- Good scripting (Python/Perl/Tcl) skills and must be conversant with UNIX;
- Knowledge of C/C++ is a plus

Requirements:
- Good knowledge of digital electronics concepts and familiarity with ASIC design flow, and/or VLSI design methodologies is required
- Ability to work in a fast paced, technology-driven environment and strong motivation to continuously learn and apply advanced digital design concepts and best practices
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28561BR/62BR/63BR/64BR: R&D Engineer II (Equipo SO) 4 vacantes

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Job Description and Requirements

The candidate will be a member of the synthesis optimization team in Digital Design Group working on projects in the fields of logic synthesis and physical aware synthesis. The successful candidate will have a great opportunity to be exposed to new and exciting initiatives that will have a huge impact on our customers. The candidate will join a highly experienced software development team and contribute to create the world’s most advanced technologies for chip design that power Artificial Intelligence, self-driving cars, cloud computing, Internet of Things and 5G that shape the life today and in the future.

Responsible for designing, developing, troubleshooting, or debugging software programs. A successful candidate will be expected to have a strong commitment to design efficient data structures and algorithms, analyze software issues, and deliver top quality production software which exceeds customer expectations.

Key Qualifications

- Design of algorithms with efficient data structures. It is desired to have EDA software background.
- Has strong motivation to learn and explore new technologies and demonstrates good analysis and problem-solving skills.
- Working knowledge in developing and maintaining complex C++/C applications in a Unix/Linux environment.
- Experienced with software development lifecycle, and proficiency with debugging and software utility tools
- Attentive to stringent performance and quality requirements.
- Builds productive internal/external working relationships through collaboration, excellent teamwork
- Strong communication skills, verbal and written in English. Ability to produce design documents detailing product requirements.
- BS or MS in Computer Science/Electrical Engineering or related field. Usually developing professional expertise and may apply company policies and procedures to resolve a variety of issues.

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